TABLE 32. Centrally administered high-performance computing systems in academic institutions, by type of institution and computing architecture: FY 2013

Type of institution	Number of institutions	Number of institutions with HPC	Type of architecture				Number of institutions with HPC accelerators
			Cluster	MPP	SMP	Other	
All institutions	581	162	157	25	30	36	124
Doctorate granting	398	150	146	19	28	33	116
Public	272	104	102	13	17	17	83
Private	126	46	44	6	11	16	33
Nondoctorate granting	183	12	11	6	2	3	8

HPC = high-performance computing; MPP = massively parallel processors; SMP = symmetric multiprocessors.

NOTES: Each institution is counted only once in each architecture. Centrally administered HPC systems are located within a distinct organizational unit with a staff and budget; the unit has a stated mission that includes supporting HPC needs of faculty and researchers. HPC is defined as systems of 10 teraflops or faster. Institutions may have HPC systems of more than one type of architecture. Accelerators may be system components or independent.

SOURCE: National Science Foundation, National Center for Science and Engineering Statistics, Survey of Science and Engineering Research Facilities, FY 2013.